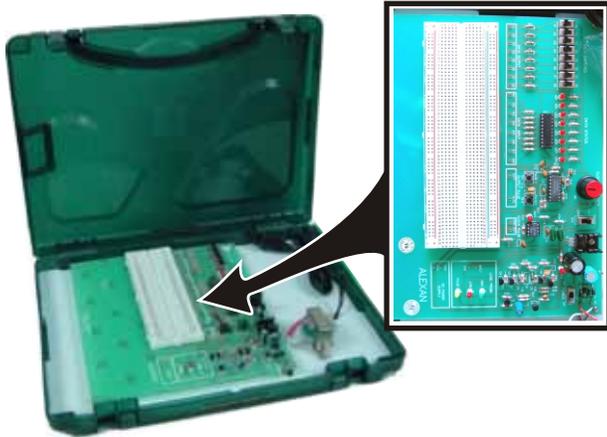


Alexan DIGITAL TRAINER

NOW AVAILABLE AT ALEXAN



Digital Experiment Board is a general-purpose digital experiment board equipped with different built-in circuits as well as a standard PB-501 breadboard. There are:

- **8 Data Switches**
The data switch circuits provides eight DATA OUTPUT pins D1 to D8 whose output level is adjusted using slide switches.
- **8 Data Status Monitor**
Has eight buffered inputs with LED indicators that show the current state of the inputs.
- **555-Based Clock Generator**
The circuit is based on a 555 IC configured as an astable multivibrator. The signal is accessible through the OUT pin. An overlapping LOW (10 Hz to 500 Hz) and HIGH (20 Hz to 1 kHz) range switch is provided for frequency control as well as VR1 for fine tuning control.
- **Pulse Generator**
A monostable signal generator based on the 74HC221 IC. There are two outputs. Pressing a switch generates a pulse across the corresponding output pin.
- **Logic Probe**
The probe displays the status of the signal fed across its input pins using three LED status indicators, one each for HIGH, LOW and PULSE.
- **5-Volt Power Supply**
This supply is the same power source used for the rest of the modules such that the user need not worry about compatibility problems when using the built-in modules in the trainer.

Also available!! **Digital Trainer Experimental kit**



Looking for affordable development tools?

Atmel AVR ISP IN-SYSTEM PROGRAMMER



- Features**
- Interface with AVR Studio
 - ISP Programming of all In-System Programmable AVR Devices
 - Programs both Flash and EEPROM
 - Supports Fuse and Lock Bit Programming
 - Supports RC Oscillator Calibration
 - Adjustable Speed Supports all Target Boards Running at a Speed Higher than 8kHz.
 - RS-232 Interface
 - Powered from Target
 - Upgradeable to support future devices

The AVR ISP gives the designer a compact and reliable programming tool to program all In-System Programmable AVR microcontrollers through a 6- or 10-pin ISP connector. Interfaces with AVR Studio for code writing and debugging. The programming software can be controlled from both a Windows environment and a DOS command-line interface.

ALL-100 UNIVERSAL & GANG PROGRAMMER



- Features**
- Utilize flexible pin drivers to get accurate waveforms, high programming speed plus over current protection, wrong insertion detection, self diagnostic etc. Ensures ALL-100 excellent performance

AT-701 EPROM ERASER



- Features**
- Electronic timer adjustable from 10 to 60 minutes.
 - Two layer design fully utilizes both top and bottom areas of the UV tube: Top layer capacity: Up to 8 pieces of 32 pin DIP EPROMs. Bottom layer capacity: Up to 9 pieces of 40 pin DIP EPROMs
 - Top layer features a stair type of receptacle to ensure all 24, 28, or 32 pin EPROMs are positioned correctly for maximum UV exposure.

- Anti-static sponge surfaces provide EPROMs with protection from static penetration.
- Power-on LED indicators.
- Lightweight, space-saving, and durable plastic body design.

- Wide range of programmable devices from 8 pins up to 300 pins covering EPROM, EEPROM, Serial PROM, flash, PLD/CPLD/FPGA, MPU/MCU, etc. in DIP, SDIP, SOP, SSOP, TSOP, PLCC, QFP, or BGA etc. package types
- Over 500 ADAPTERS, 150 CONVERTERS can be used with single socket programming module or Gang Programming Module to ensure your capital investment retained.
- Runs on Windows. After master read or file download from PC, user only needs to select (Blank Check, Program, Auto) function from Menu and then hit YES key on programmer to start program function. Operation is very easy.
- Via tiered star USB topology to achieve highest throughputs.
- Receive new device support S/W at no charge
- Distribution channel covers 35 countries worldwide. Your problem could be taken care anywhere the world
- Wide device covering range, flexible future expansion capability help users to achieve challenging time-to-market goal